

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

O.I.P.E. PMS4
 SCANNED GAO Q.A. 1A

PATENT DATE

APPLICATION NO. 09/876019	CONT/PRIOR F	CLASS 257	SUBCLASS: 315	ART UNIT 2814	EXAMINER H. PHAM
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TIME APPLICANTS

Seiichi Mori


Semiconductor memory integrated circuit and its manufacturing method

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)						
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip Inside File Jacket

 TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ _____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid .
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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